

2841
Jew**AMENDMENT TRANSMITTAL LETTER**Docket No.
SONYJP 3.0-217Application No.
09/993,094Filing Date
November 6, 2001Examiner
Phuong T. VuArt Unit
2841

Applicant(s): Shingo Matsumoto, Yasushi Fujita, and Katsumi Endo

Invention: ADAPTER DEVICE, MEMORY DEVICE AND INTEGRATED CIRCUIT CHIP

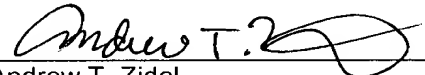
TO THE COMMISSIONER FOR PATENTS

Transmitted herewith is an amendment in the above-identified application.

The fee has been calculated and is transmitted as shown below.

CLAIMS AS AMENDED					
	Claims Remaining After Amendment	Highest Number Previously Paid	Number Extra Claims Present	Rate	
Total Claims	9	- 9 =	0	x	0.00
Independent Claims	5	- 5 =	0	x	0.00
Multiple Dependent Claims (check if applicable) <input type="checkbox"/>					
Other fee (please specify):					
TOTAL ADDITIONAL FEE FOR THIS AMENDMENT:					0.00

- ☒ Large Entity ☐ Small Entity
- ☒ No additional fee is required for this amendment.
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as described below. A duplicate copy of this sheet is enclosed.
- ☒ Credit any overpayment.
- ☒ Charge any additional filing or application processing fees required under 37 CFR 1.16 and 1.17.


Andrew T. Zidel
Attorney Reg. No.: 45,256

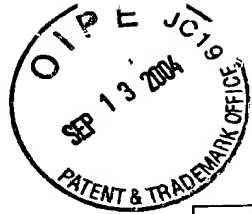
Dated: September 9, 2004

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Dated: September 9, 2004

Signature:

Andrew T. Zidek
(Andrew T. Zidek)

Docket No.: SONYJP 3.0-217
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Matsumoto et al.

Application No.: 09/993,094

Filed: November 6, 2001

For: ADAPTER DEVICE, MEMORY DEVICE
AND INTEGRATED CIRCUIT CHIP

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: Group Art Unit: 2841
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: Examiner: Phuong T. Vu
:
:

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY PURSUANT TO 37 C.F.R. § 1.111

Dear Sir:

In response to the Office Action mailed June 29, 2004,
applicants submit the following remarks.